



INFORMATION DISCLOSURE STATEMENT		Case No. Serial No. Applicant: Filing Date: Group:
		32 09/450,054 A. V. Krishnamoorthy November 29, 1999

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translati n
	AF						
	AG						
	AH						

OTHER (including Author, Title, Date, Pertinent Pages, etc.)

<i>CHD</i>	AI	A. L. Fisher et al, "Synchronizing Large VLSI Processor Arrays", <i>IEEE Trans. Comp.</i> , Vol. C-34, No. 8, pp. 734-740 (August 1985)
<i>CHD</i>	AJ	A. V. Krishnamoorthy et al, "Vertical-Cavity Surface Emitting Lasers Flip-Chip Bonded to Gigabit/s CMOS Circuits", <i>IEEE Photonics Technology Letters</i> , Vol. 11, No. 1, January 1999
	AK	
	AL	
	AM	
	AN	

EXAMINER	<i>CHD</i>	DATE CONSIDERED
		<i>12/13/01</i>

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609:
 Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.